



तेजपुरविश्वविद्यालय/ TEZPUR UNIVERSITY

(केंद्रीयविश्वविद्यालय/A Central University)

कुलसचिवकाकार्यालय/OFFICE OF THE REGISTRAR

तेजपुर-784028 :: असम/ TEZPUR-784028 :: ASSAM

CORRIGENDUM NOTICE

(ET-NIQ-.....552..... DT- 16-06-2020)

The Technical Specifications (TechSheet) mentioned/uploaded alongwith NIQ No. **ET-NIQ-421; DT-08.06.2020 (Tender ID: 2020_TEZU_563903_1)** has been replaced with a **Revised Technical Specification (TechSheet)**.

The other contents of the NIQ remain unaltered.

[Handwritten Signature]
16/06/20

Assistant Registrar (GA)

Tezpur University

Memo No: TU/11-24/Pur/Qtn(ET)/2020-21/

Copy Information to:

1. Webmaster, Tezpur University for uploading the notice in the website.
2. Dr. P.P Sahu, Department of Electronics and Communications Engineering.
3. File

Dated:- 16.06.2020

[Handwritten Signature]
16/06/20

Assistant Registrar (GA)

Technical Specifications cum Compliance Report
(To be submitted on Company's/Firm's Letterhead Signed and Sealed)

Items:

Item	Detail Specifications	Compliance Yes/No	Remarks; If any;
FPGA Kit/Board Starter Kit	<p>1. FPGA board - 05 no.</p> <ul style="list-style-type: none"> (a) Xilinx Z-7010 (667 MHz) (b) Dual-core ARM Cortex-A9 processor (c) 256 MB Nonvolatile memory and 512 MB DDR3 RAM (d) Wireless Connectivity with the host computer (e) 10 analog inputs(Sampling Rate: 500 kS/s) (f) 6 analog outputs(Sampling Rate: 345 kS/s) (g) 40 digital I/O lines (h) Programmable with LabVIEW software (i) Onboard Accelerometer present , LEDs, push button (j) the 0 to 40 °C operating temperature range (k) Camera for image & video processing <p>LabVIEW advanced graphical programming platform software research as a bundle with all tool kit and 01 year SSP (01 user)</p> <p>2. FPGA Starter Kit – 05 no.</p> <ul style="list-style-type: none"> (a) Platform: <u>Xilinx Zynq-7000</u> AP SoC XC7Z020-CLG484 (b)System:Dual-core ARM Cortex™-A9 or higher (c)L1 Cache:32 KB instruction,32KB data per processor (d) L2 cache: 512 KB or higher (e) On chip Memory: 256 KB or higher (f) Board Memory: 512 MB DDR3, 256 MB Quad-SPI Flash , SD card connector (g) Max. Freq.: 866 MHz or higher (h) Clock Frequency: One 100 MHz oscillator for PL, one 33.33 MHz oscillator for PS (i) Display or Audio interface: 1080p HDMI Output, 8-bit VGA, 128x32 OLED Display, Audio Lin-in, Line-our, microphone (j) Interface: On-board USB-JTAG Programming, USB OTG 2.0 (k) Ethernet: 10/100/1000 or higher (l) Push Button: 05 or higher (m) User LEDs: 8 or higher 		

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16/06/xx