



**Tezpur University**

**Department of Electronics &  
Communication Engineering**

# **DIGITAL VLSI SYSTEM DESIGN USING CADENCE TOOL & FPGA**



**Dr. Sonali Chouhan**  
Associate Professor  
IIT Guwahati



**Dr. Anup Dandapat**  
Associate Professor  
NIT Meghalaya



**Dr. Prabir Kumar Saha**  
Assistant Professor  
NIT Meghalaya

**17th March to 21st March, 2023**

**Venue - ECE Department**

**Students from the ECE Department pursuing B.Tech (6th &  
8th Semesters) and M.Tech can apply.**

**Seats are limited!**

**Registration closes on 14th March.**



The applicants can apply through the QR  
or the link provided. Your confirmation  
would be based on first come first serve  
basis.

<https://forms.gle/qzEJfedQZbkCHR4A9>

**Contact - Dr. Biplob Mondal**  
[biplobm@tezu.ernet.in](mailto:biplobm@tezu.ernet.in)