4 WEEK TRAINING PROGRAM ON

XILINX EDA TOOL FOR FPGA IMPLEMENTATION

ORGANIZED BY
DEPARTMENT OF ECE UNDER CHIP-TO-STARTUP (C2S) PROGRAMME

WORKSHOP DETAILS:

SCHEDULE: 2ND TO 26TH SEPTEMBER, 2025

TIMING: 1 HR PER DAY (AFTER 5.30 PM)

VENUE: DEPARTMENT OF ECE

ELIGIBILITY: B.TECH. (ECE) (3RD & 5TH SEM)
M.TECH. (ELDT/SCT/BIO) (1ST SEM)

WHAT YOU WILL LEARN:

- **VLSI DESIGN BASICS**
- **RTL TO GDS DESIGN FLOW**
- **XILINX EDA TOOL**
- **IMPLEMENTATION ON FPGA**

LIMITED SEATS (30 ONLY)

CERTIFICATES WILL BE PROVIDED

REGISTRATION LINK:

HTTPS://TINYURL.COM/AU2W3C75   **OR**

NO REGISTRATION FEE

REGISTRATION ENDS: 30TH AUGUST, 2025

RESOURCES PERSONS:
- DR. BIPLOB MONDAL
- DR. DURLAV SONOWAL
- DR. UJJWOL BARMAN

- MANASH PRATIM HAZARIKA
- BIDYUT BIKASH BORAH